



Press Contacts:

Stephen R. Pollock  
IC Manage  
[pollock@icmanage.com](mailto:pollock@icmanage.com)  
[www.icmanage.com](http://www.icmanage.com)

## ROGER MARCH APPOINTED CHIEF TECHNOLOGY OFFICER

*Brings Broad IC Design and IC Design Infrastructure Experience to IC Manage*

**Los Gatos**, Calif. October 4, 2004— IC Manage today announced that Roger March has joined IC Manage as Chief Technology Officer. Shiv Sikand, Vice President of IC Manage said, "I am happy to welcome Roger to the IC Management team. Roger is one of the most brilliant software architects that I have ever known. His insights will help us to develop enterprise solutions that will propel us to a leadership position in providing IC design management solutions.

### **About Roger March**

Roger began his career as a circuit and logic designer in the early days of microprocessors and dynamic RAMs. While working as a designer at Data General and Zilog on long forgotten products like the microNova, microEclipse and Z80K, he found himself drawn to the CAD side to provide design tools that the marketplace was not yet offering. He wrote circuit, logic and fault simulators that were used to build and verify microprocessors of the day.

Roger then joined MIPS as their first CAD engineer. Here he built their infrastructure, this time using a combination of vendor and internal tools. This system was used to build all the MIPS microprocessors as well as most of their system designs. He wrote module generators for chip physical design, placement and allocation tools for board designs, test pattern generators and coverage tools for verification, and yet another logic simulator - it was found to be 30X faster than Verilog-XL in benchmarks.

After MIPS was acquired by Silicon Graphics, Roger became a Principal Engineer at the company. Working in the microprocessor division, he worked on problems in logic verification and timing closure. This dragged him more deeply into the realm of design databases. He wrote several tools

to help analyze and manipulate physical, logical and parasitic extraction data sets. This included work in fault simulation, static timing verification, formal verification, physical floor planning and physical timing optimization.

Roger most recently worked at Matrix Semiconductor. He designed and helped build most of their CAD infrastructure. He worked mainly on the physical and process side and also on optimizing layout for manufacturability.

### **About IC Manage**

IC Manage develops and delivers a next generation enterprise IC design management system that significantly improves designer efficiency, provides widespread design reuse, and has the reliability and performance needed for large designs. IC Manage includes the industry-leading Perforce SCM system. IC Manage is located at 101 Church Street, Suite 17, Los Gatos, CA 95030, Tel: (408) 399-5401, Fax: (408) 399-5403, [www.icmanage.com](http://www.icmanage.com).

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